## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 6,975,552 B2

APPLICATION NO. : 10/644610

DATED

: December 13, 2005

INVENTOR(S)

: Tae H. Kim

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Page 1 of 2

Column, Line Item (57), Line 3	Reads (hight "and an openfline"	Should Read digit
Column 1, Lines 45, 50	"complimentary"	complementary
and 52		
Column 2, Line 40	"pair is surrounded"	pair being surrounded
Column 4, Line 30	"to which portion of a	to which portions of a
	digit line memory cells	digit line of memory cells
	are"	are
Column 4, Line 41	"aspects of a open digit	aspects of an open digit
	line"	line
Column 4, Line 64	"array 10, thus, coupling"	array 10, thus coupling
Column 5, Line 57	"memory arrays, thus,"	memory arrays, thus
Column 5, Line 59	"to active column 50, 80"	to active columns 50,
		80
Column 6, Line 27	"but are instead, shifted"	but are instead shifted
Column 6, Line 41	"are desired. For	are desired; for
	example,"	example,



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Column 6, Line 66

'MDL portion 156.

-- MDL portion 156.

virtue of the data state

Consequently, the voltage

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stored by the"

of the digit line will be

altered from the precharge

voltage by virtue of the

data state stored by the--

Column 7, Line 38

"of the memory arrays

 $\sim$  --of the memory arrays 202a,

**202***a*, **202***b* is a respective

**202***b* is a respective

row various"

row address latch 226,

which stores the row

address, and a row decoder

228 which applies various--

Column 14, Line 21

"cells of the colunm"

--cells of the column--

This certificate supersedes certificate of correction

issued December 25, 2007.

